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TITLE: DELAY-TIME CONTROL CIRCUIT, AND AUTOMATIC WIRING LAYOUT

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INT-CL (IPC): H01L 27/04; G06F 15/60; H01L 21/82; H01L 27/118; H03K 19/173

ABSTRACT:

PURPOSE: To provide a method of automatic layout for a signal delay control circuit, in which signal delay time can be adjusted without a change of cell arrangement after the completion of automatic wiring.

CONSTITUTION: Capacitor cell arrays 41-44 are arranged in wiring channel regions 31-34 between cell arrays 21-25. Before automatic layout, nets N1 and N2 liable to be affected by signal delay are specified, and an allowable delay time between the specified nets is determined. When automatic layout is performed, capacitor selection cells X1 and X2 having switch elements are arranged. The specified nets N1 and N2 are connected with capacitor cells Y1 and Y2 through the selection cells. The on-off states of the switch elements are fixed to satisfy the allowable differential time delay.

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S14771	U	USPT		6618847 and (ground near6 power)	2005-07-25 14:25:37	
S14770	U	USPT		6618847 and (cell$1)	2005-07-25 14:12:10	
S14769	U	USPT		6618847 and (calculat$5)	2005-07-25 14:08:07	
S14768	U	USPT		6618847 and (drive or load or capacity)	2005-07-25 14:07:46	
S14767	U	USPT		6618847 and logic	2005-07-25 14:07:06	
S14766	U	USPT		6618847 and capacitance	2005-07-25 14:05:16	
S14765	U	USPT		6618847 and logic	2005-07-25 14:04:32	
S14764	U	USPT		6618847 and (capacitor adj cell$1)	2005-07-25 13:33:49	
S14763	U	USPT		6618847 and calculat$5	2005-07-25 13:32:55	
S14762	U	USPT		6618847 and automatically	2005-07-25 13:32:19	
S14761	U	USPT		6618847 and automatic	2005-07-25 13:32:09	
S14760	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(capacitor near6	2005-	

			cell$1) and logic and (automatic$5 or automated) and layout and (empty adj space)	07-25 13:10:54
S14759	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(capacitor near6 cell$1) and logic and (automatic$5 or automated) and layout and (kerf or unused or between)	2005-07-25 13:09:57
S14758	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(capacitor near6 cell$1) and logic and (automatic$5 or automated) and layout	2005-07-25 13:09:03
S14757	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(capacitor near6 cell$1) and logic and (automatic$5 or automated)	2005-07-25 13:08:33
S14756	U	USPT	5587333	2005-07-25 13:05:52
S14755	U	USPT	(5587333| 6232154| 6353248| 6523159| 20030148278)! [pn]	2005-07-25 13:04:26
S14754	U	USPT	6898769	2005-07-25 13:04:16
S14753	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(6009251| 6096580| 6289412| 6321371| 6350693| 6355558| 6378110| 6396096| 6459331| 6574140| 6591406| 20020107396| 20020131291)! [pn]	2005-07-25 12:53:54
S14752	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(computer same	2005-

			layout same (capacitor adj cell$1)) and logic	07-25 12:53:23
S14751	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	computer same layout same (capacitor adj cell$1) same logic	2005-07-25 12:52:59
S14750	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(5361234| 5998846| 6157067)![pn]	2005-07-25 12:50:56
S14749	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	6353248	2005-07-25 12:49:49
S14748	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(capacitor adj cell$1) and automatic$5 and logic and calculat$5	2005-07-25 12:39:43
S14747	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(capacitor adj cell$1) and automatic$5 and logic	2005-07-25 12:39:24
S14746	U	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD	(capacitor adj cell$1) and automatic$5	2005-07-25 12:39:12
S14745	U	JPAB,DWPI	▆▆▆▆	2005-07-25 12:27:53
S14744	U	JPAB,DWPI	▆▆▆	2005-07-25 12:27:17
S14743	U	JPAB,DWPI	▆▆▆	2005-07-25 12:26:49
S14742	U	PGPB,USPT	▆▆▆▆	2005-07-23 15:33:47
S14741	U	PGPB,USPT	▆▆▆▆	2005-07-23 15:31:56
S14740	U	PGPB,USPT	▆▆▆▆	2005-07-23 15:29:16
S14739	U	PGPB,USPT	▆▆▆	2005-07-23